

Roll No.

Total Pages : 2

BT-8/D-22

48319

MIXED SIGNAL DESIGN

Paper-ECO-16A

Time : Three Hours]

[Maximum Marks : 75

Note : Attempt *five* questions in all, selecting *one* question from each Unit. All questions carry equal marks.

UNIT-I

1. What is Sampling Switches? Explain MOSFET as switches and describe charge injection cancellation.
2. What are switched capacitor amplifiers explain unity gain sampler/buffer?

UNIT-II

3. Explain basic CMOS comparator design along with its working and design issues.
4. What is the operating principle of PLL explain its working and applications.

UNIT-III

5. Explain specifications and architectures of DAC.

6. Discuss design and operation of charge-scaling DACs.

UNIT-IV

7. What do you mean by flash ADC? Explain two-step flash ADC.
8. Discuss ADC specifications and architectures.

